

FIG. 1  
(PRIOR ART)

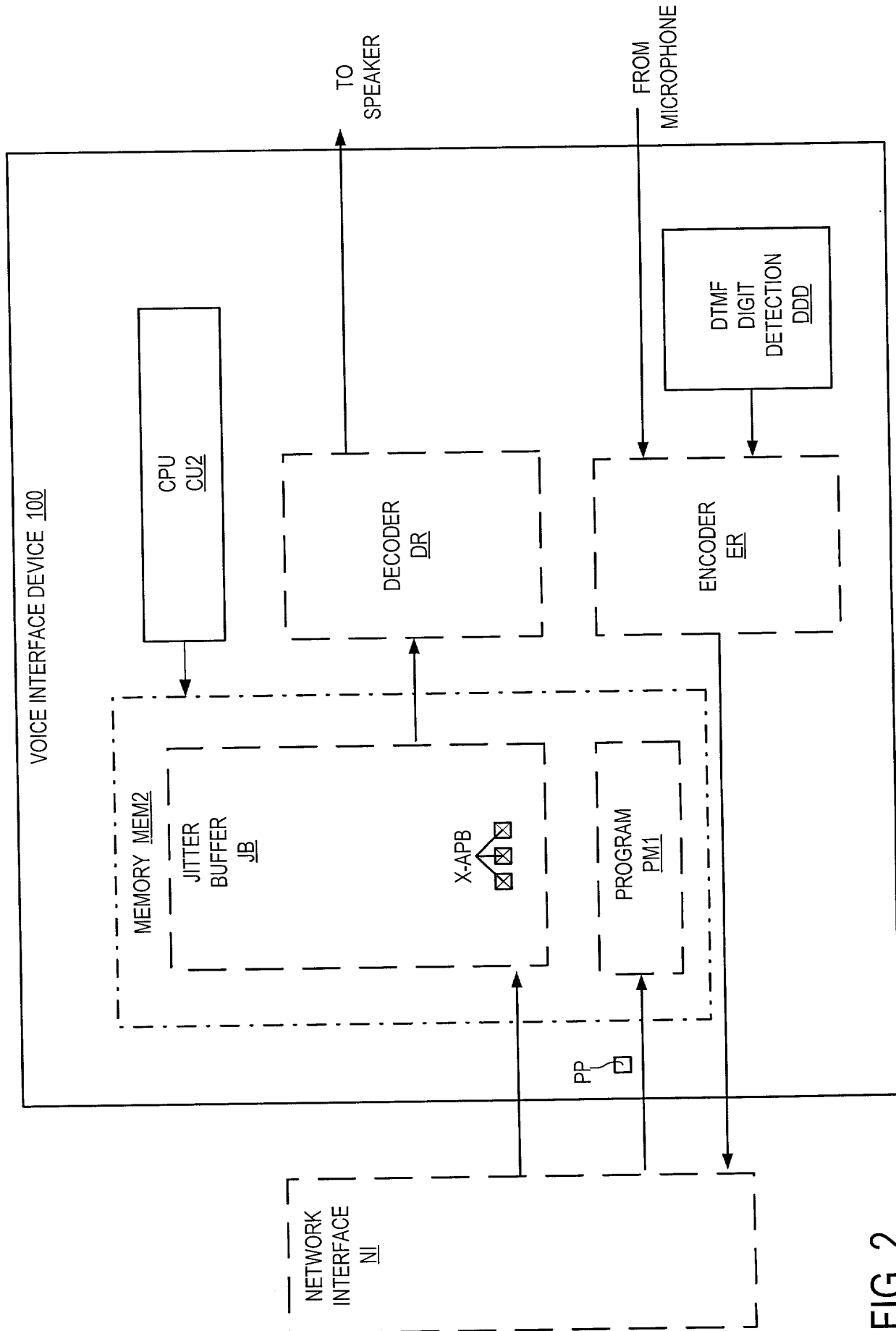
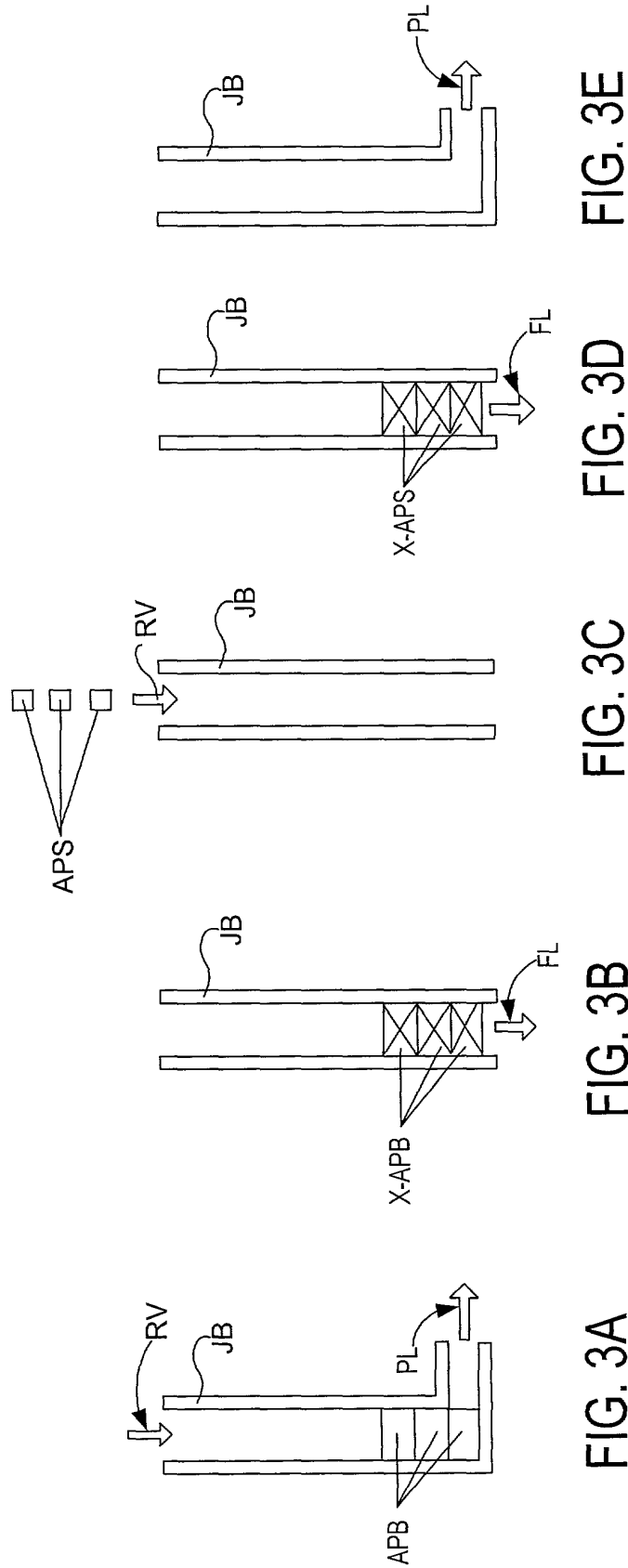


FIG. 2



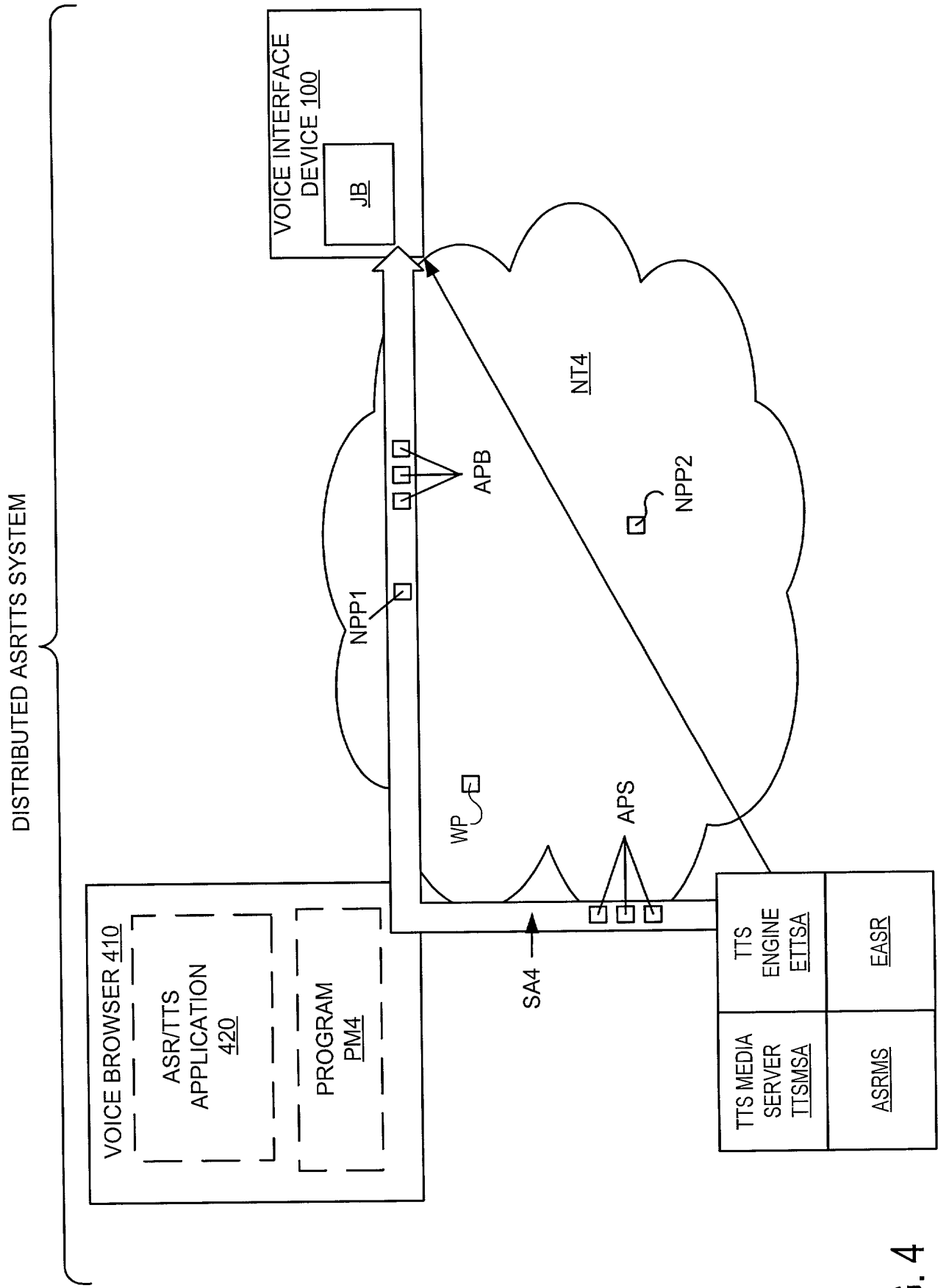


FIG. 4

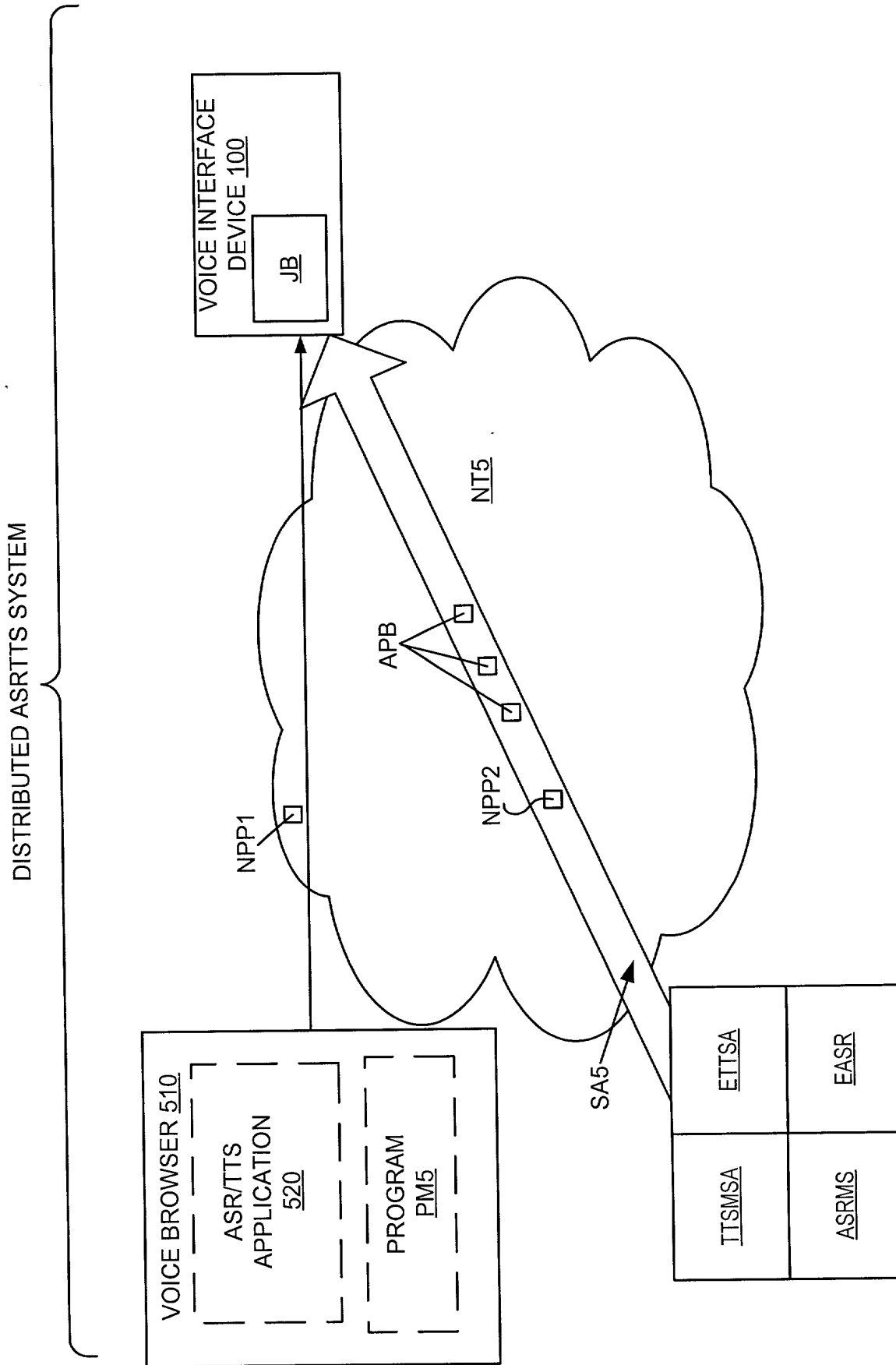


FIG. 5

DISTRIBUTED ASR/TTS SYSTEM

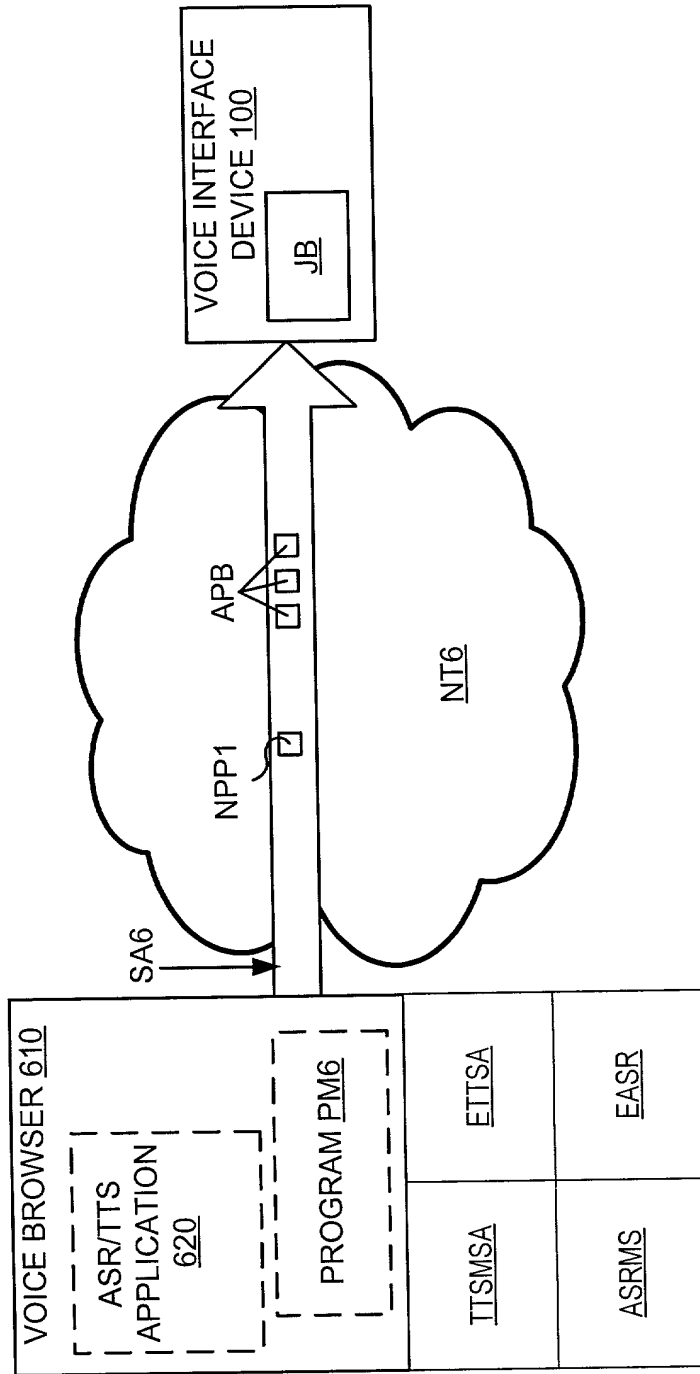


FIG. 6

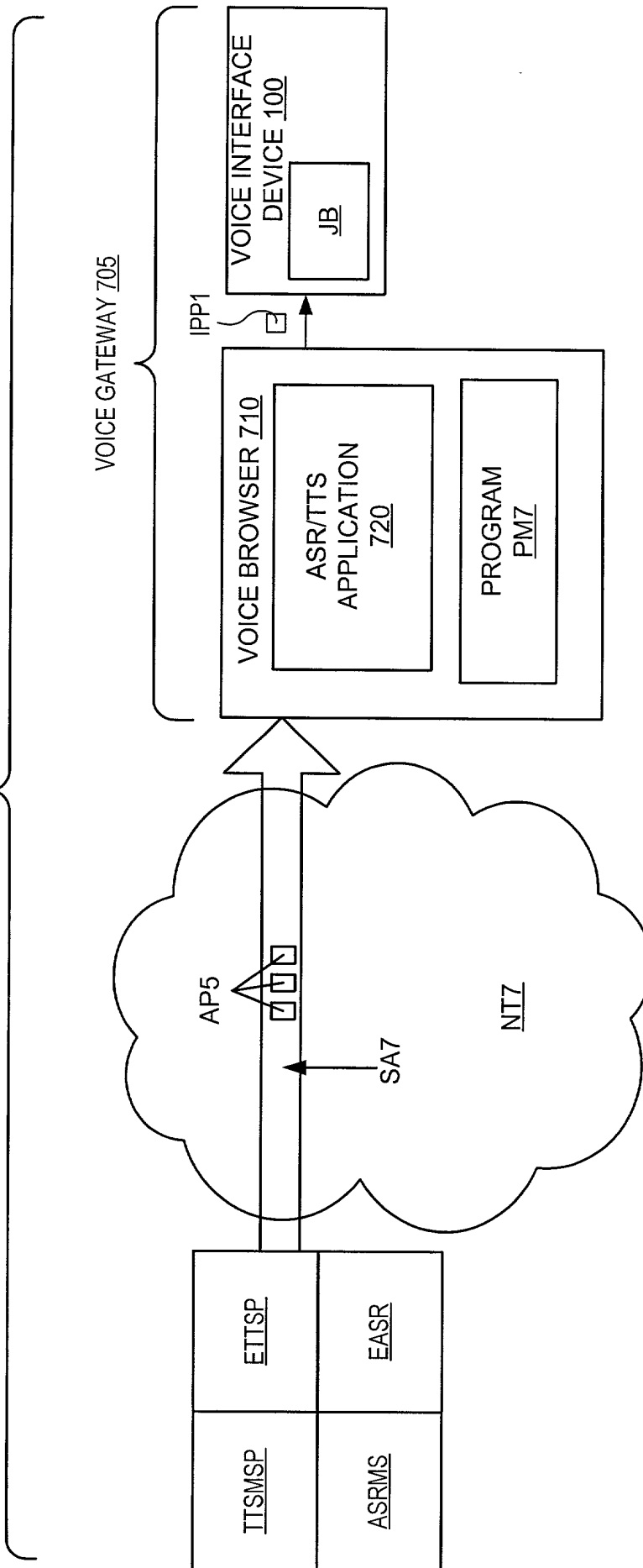


FIG. 7

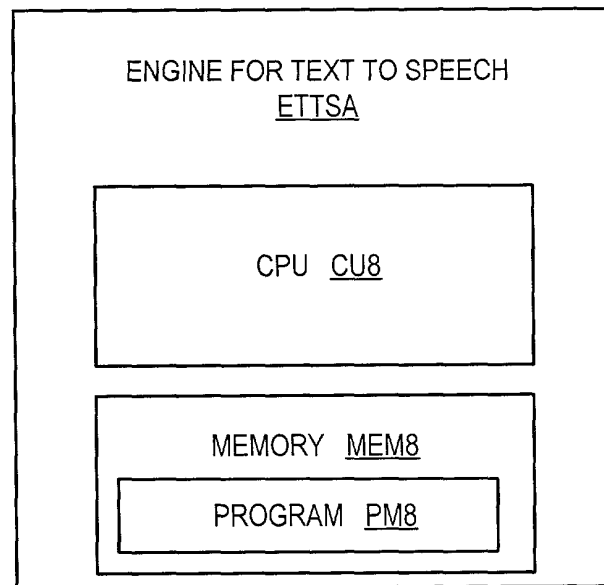


FIG. 8



900

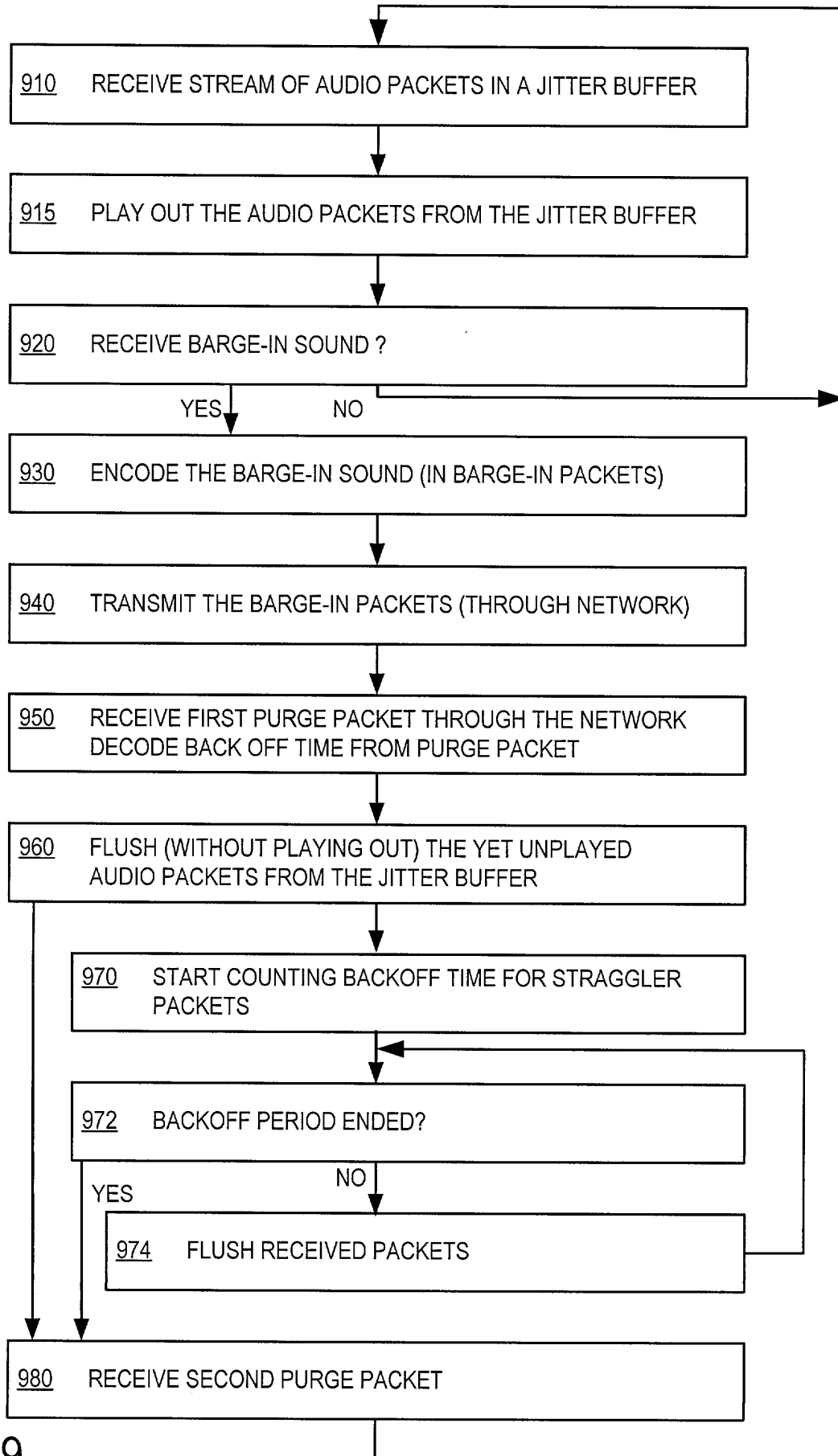


FIG. 9

1000

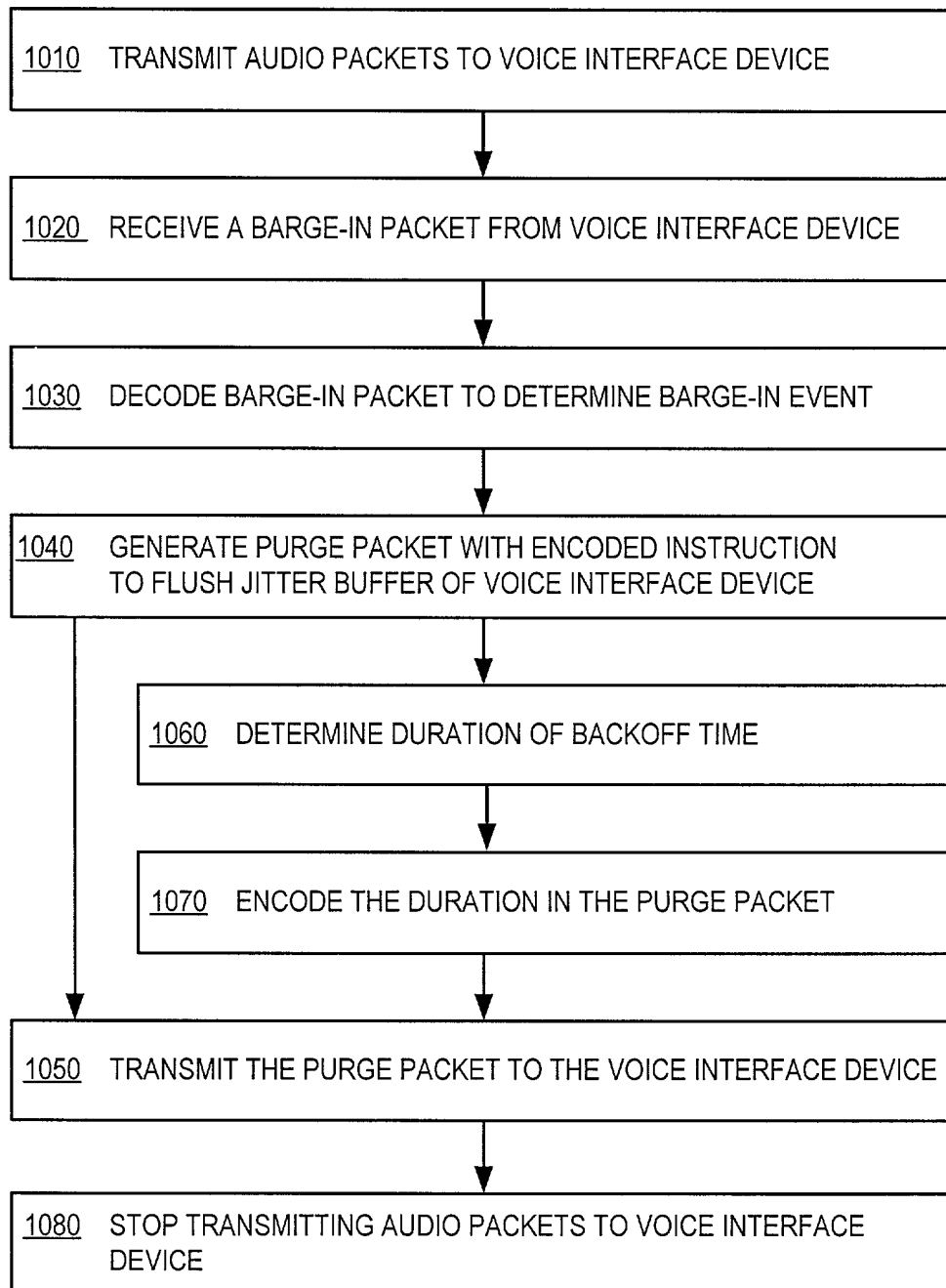


FIG. 10